

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Koichi NANIWAE

Conf. 9283

Application No. 10/573,492

Group 2892

Filed May 15, 2006

Examiner Eric W. Jones

METHOD OF CLEANING TREATMENT AND METHOD FOR MANUFACTURING
SEMICONDUCTOR DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 30, 2009

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed Form PTO-1449.

A concise explanation of the relevance of these items is that these references were cited by the Japanese Patent Office in the corresponding Japanese Application Serial No. 2005-509175, filed June 30, 2009. A copy of the Japanese Official Action in which they were cited is attached hereto, with what is believed to be the pertinent portion enclosed in a wavy line. An English translation of the enclosed portion is also attached hereto.

Under the provisions of 37 CFR 1.97(e), the undersigned hereby certifies that each item of information

contained in this Information Disclosure Statement was first cited in any communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this Statement, and under the provisions of 37 CFR 1.704(d), the undersigned further certifies that this communication was not received by any individual designated by 37 CFR 1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement.

Please charge the fee of \$180 required by 37 C.F.R. § 1.17(p) to our credit card set forth in the attached Credit Card Payment Form.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future submissions, to charge any underpayment or credit any overpayment to Deposit Account No. 25-0120 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17.

Respectfully submitted,

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